

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Tadashi IGUCHI)	Group Art Unit: 2826
)	
Application No.: 10/612,033)	Examiner: Kevin V. QUINTO
)	
Filed: July 3, 2003)	
)	
For: SEMICONDUCTOR MEMORY)	Confirmation No.: 5852
PREVENTING AN ELECTRIC SHORT)	
CIRCUIT BETWEEN A WORD LINE)	
AND A SEMICONDUCTOR)	
SUBSTRATE, AND MANUFACTURING)	
METHOD FOR THE)	
SEMICONDUCTOR MEMORY)	

Mail Stop RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

AMENDMENT

Concurrent with the Request for Continued Examination (RCE) filed herewith and in reply to the Final Office Action mailed May 27, 2008, the period for response having been extended to September 29, 2008 by a request for extension of time of one (1) month (September 27 being a Saturday) and fee payment filed concurrently herewith, and pursuant to 37 C.F.R. § 1.116, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims in this paper.

Remarks follow the amendment section of this paper.